

FIG. 1

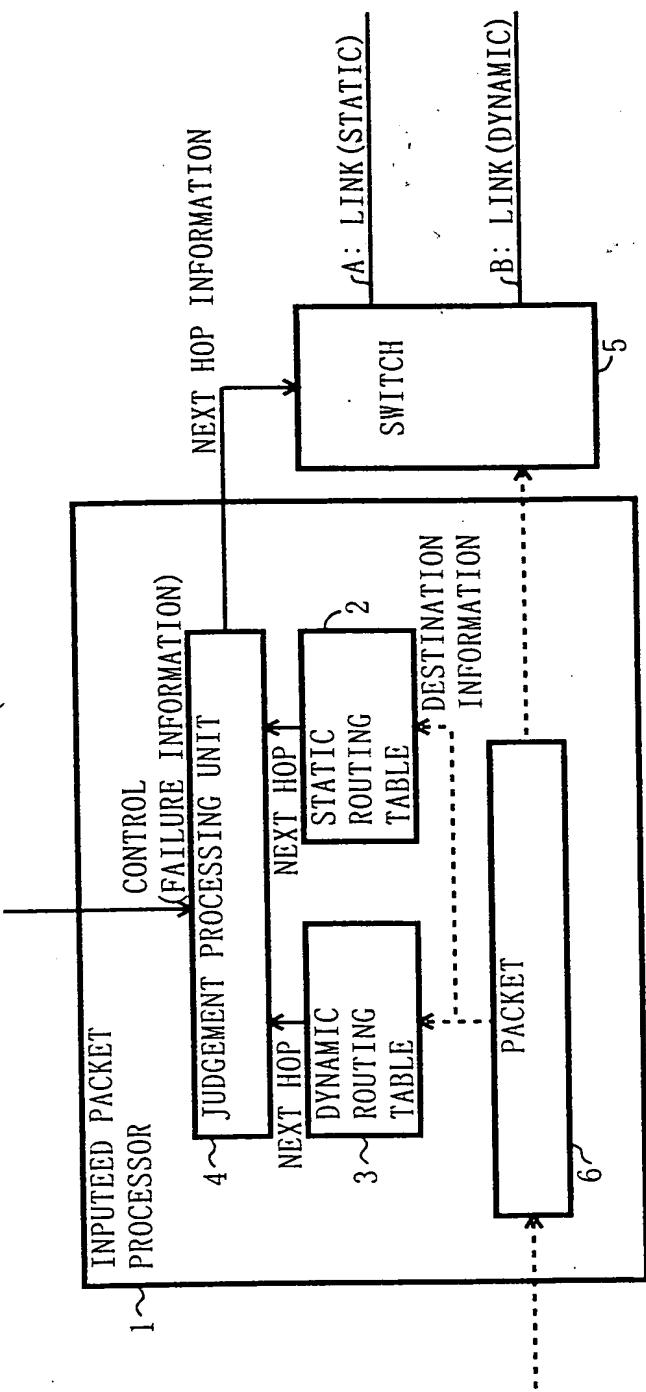
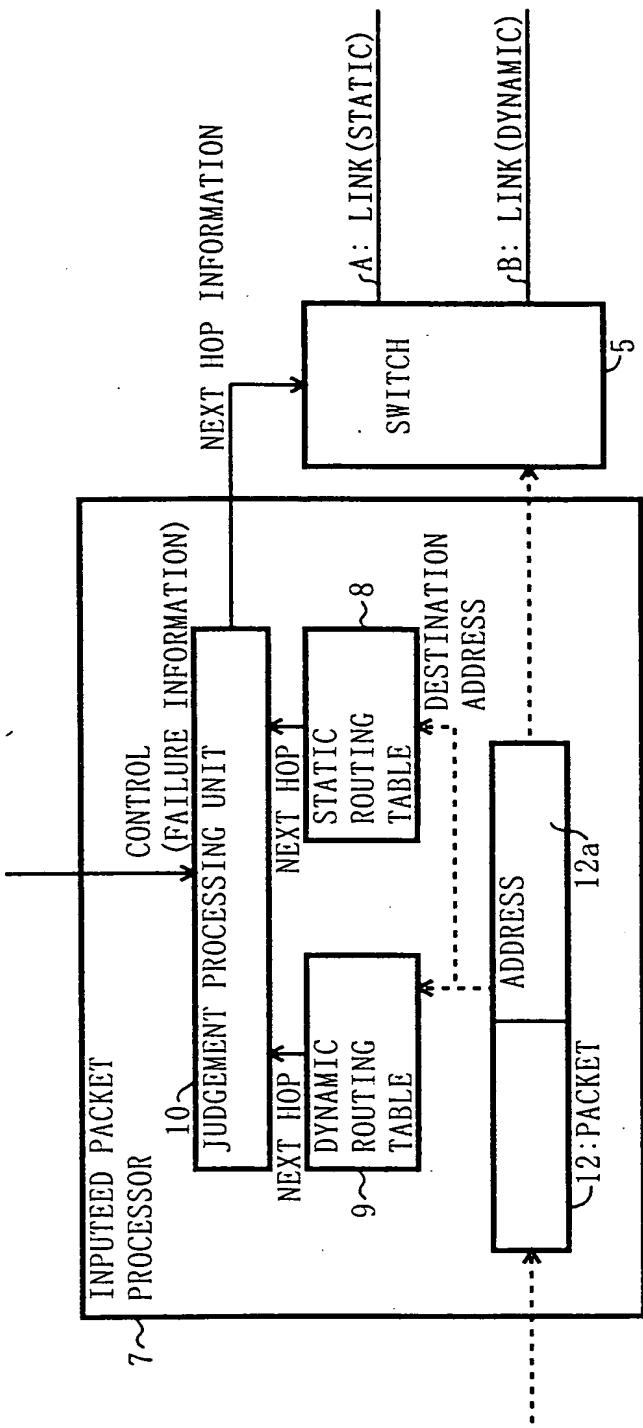


FIG. 2



F I G . 3

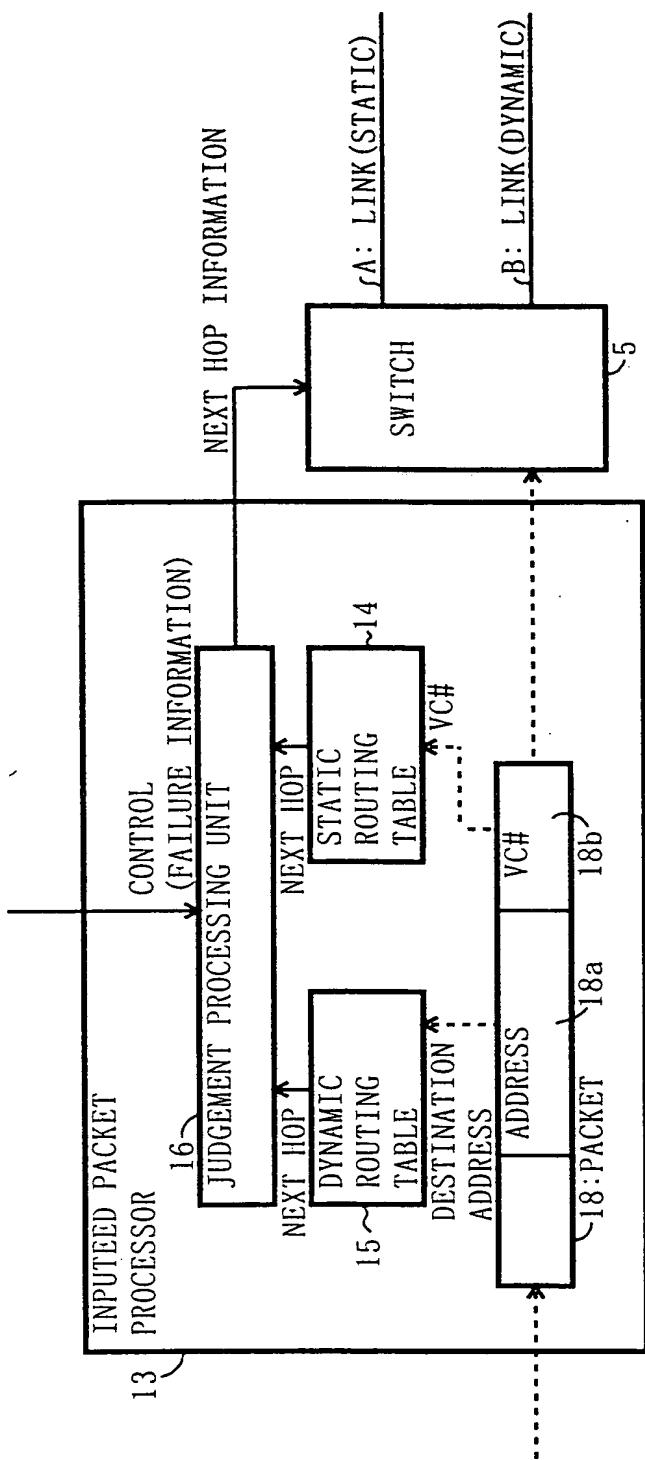


FIG. 4

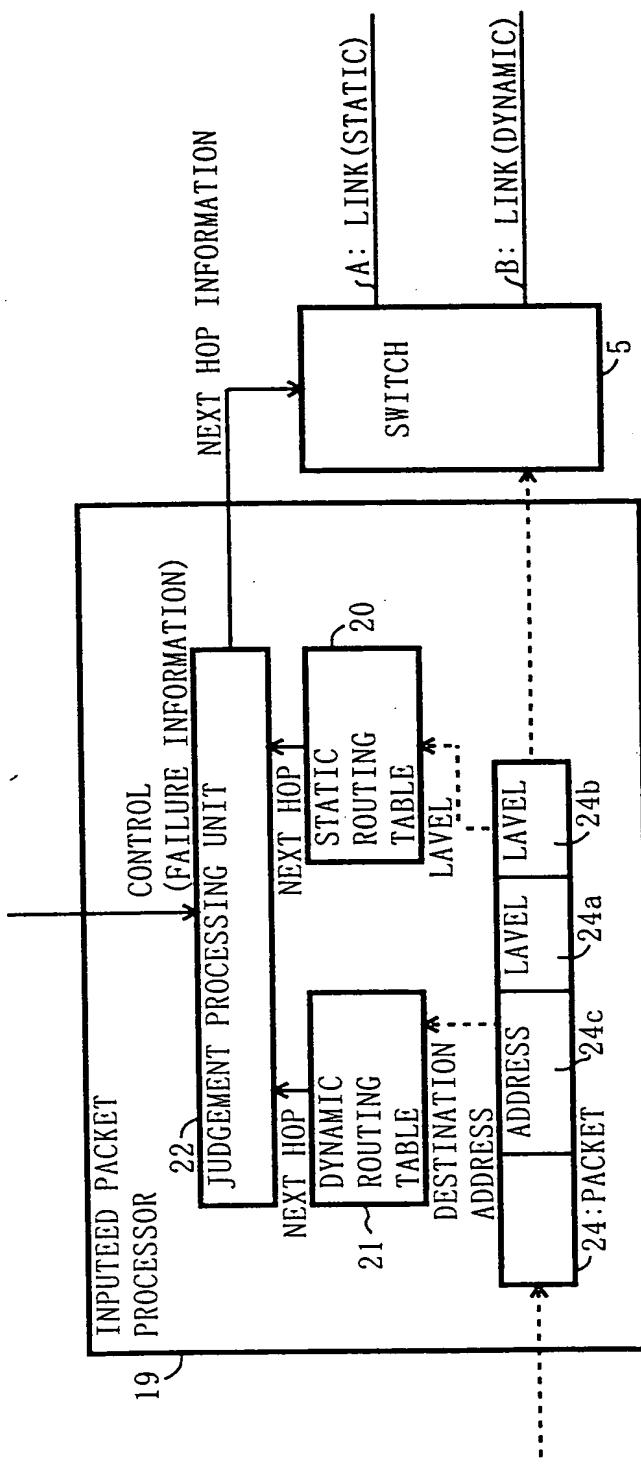


FIG. 5

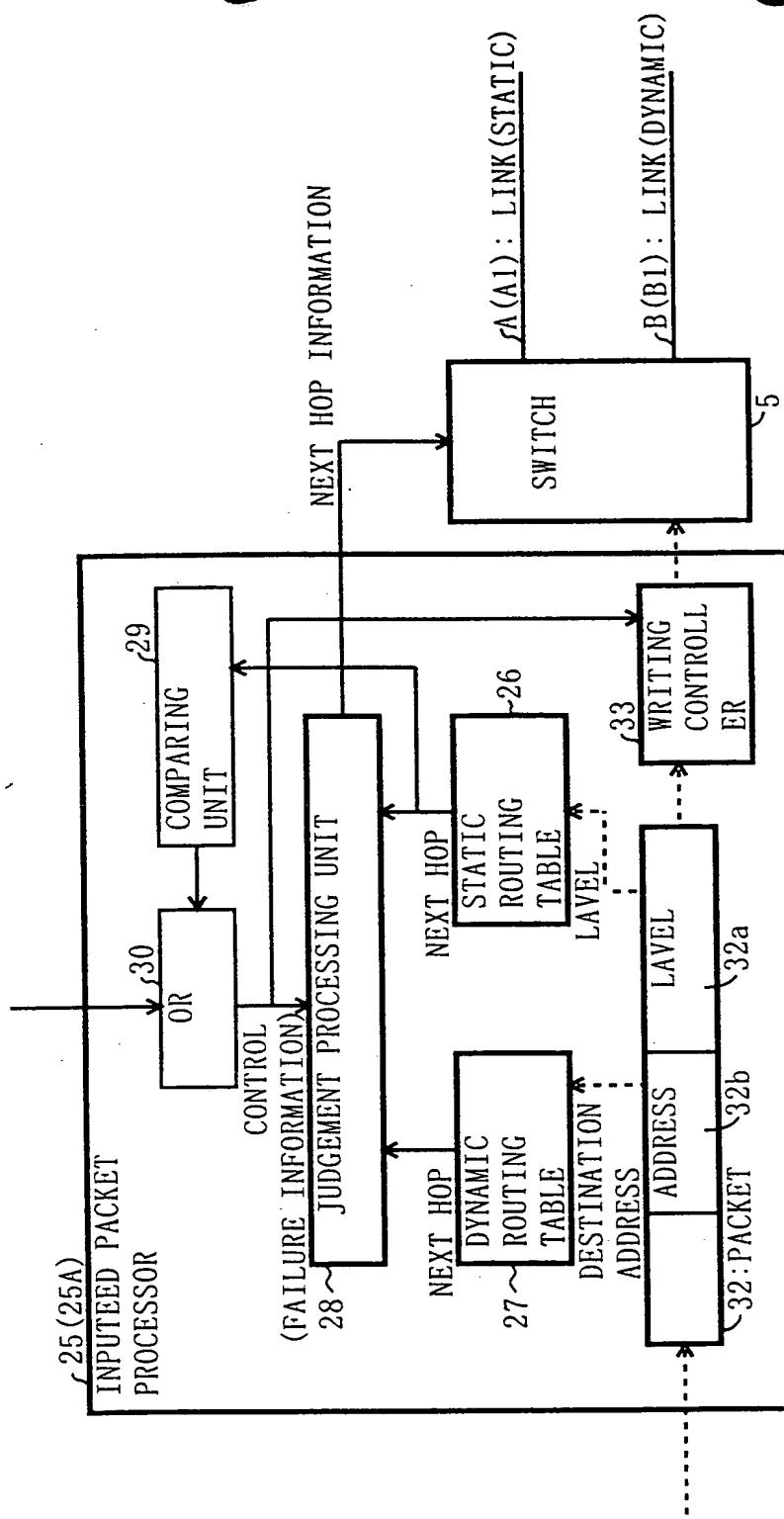


FIG. 6

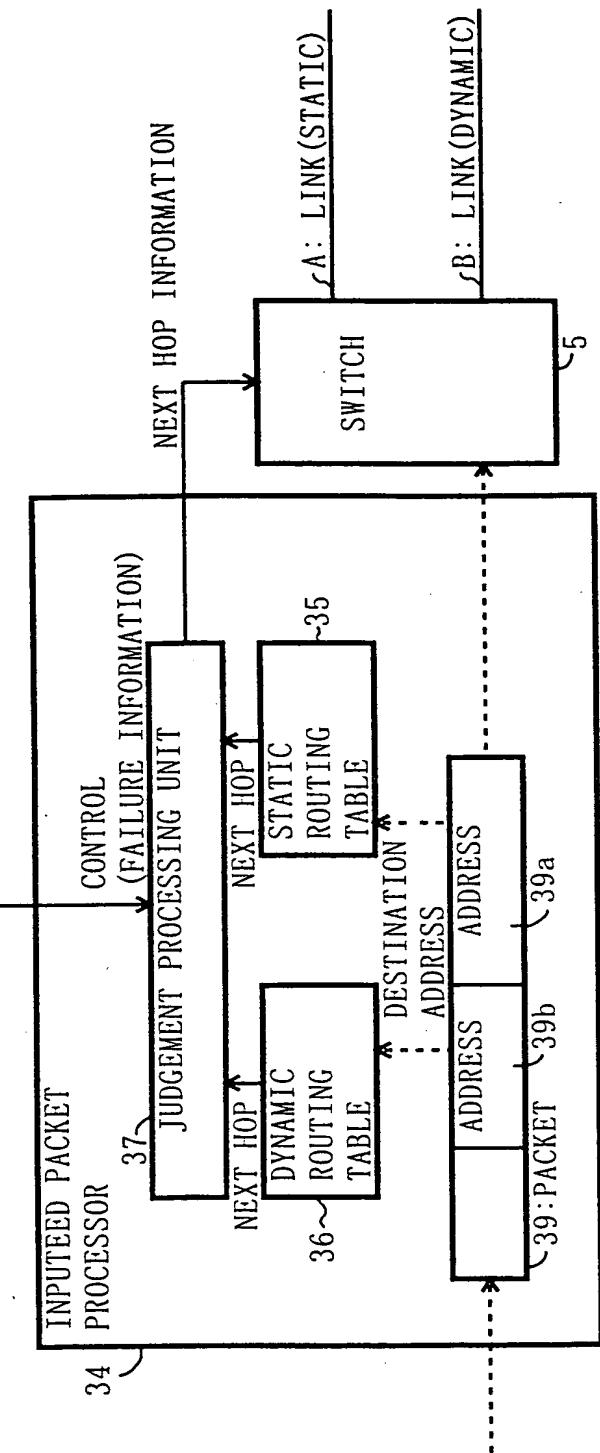


FIG. 7

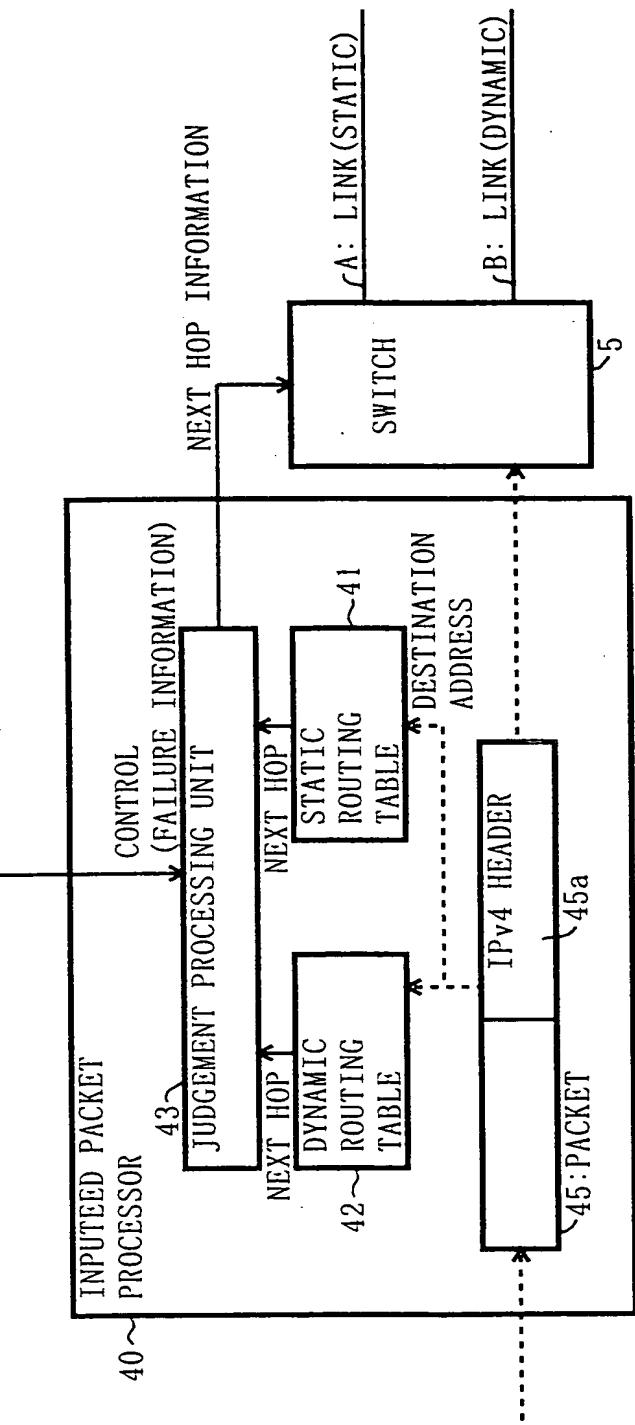


FIG. 8

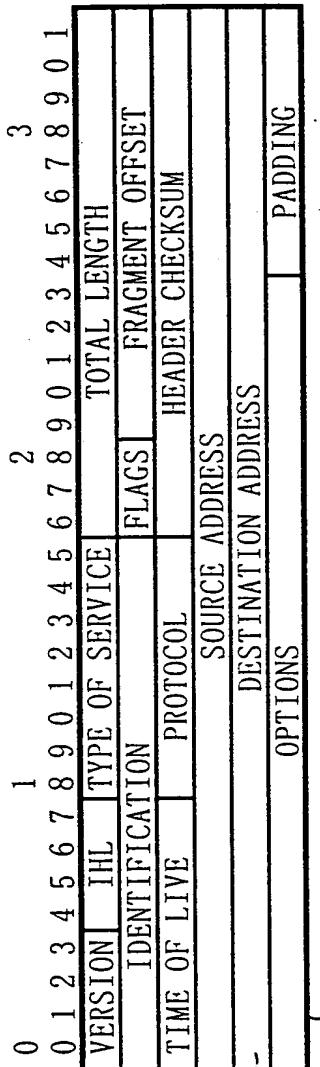
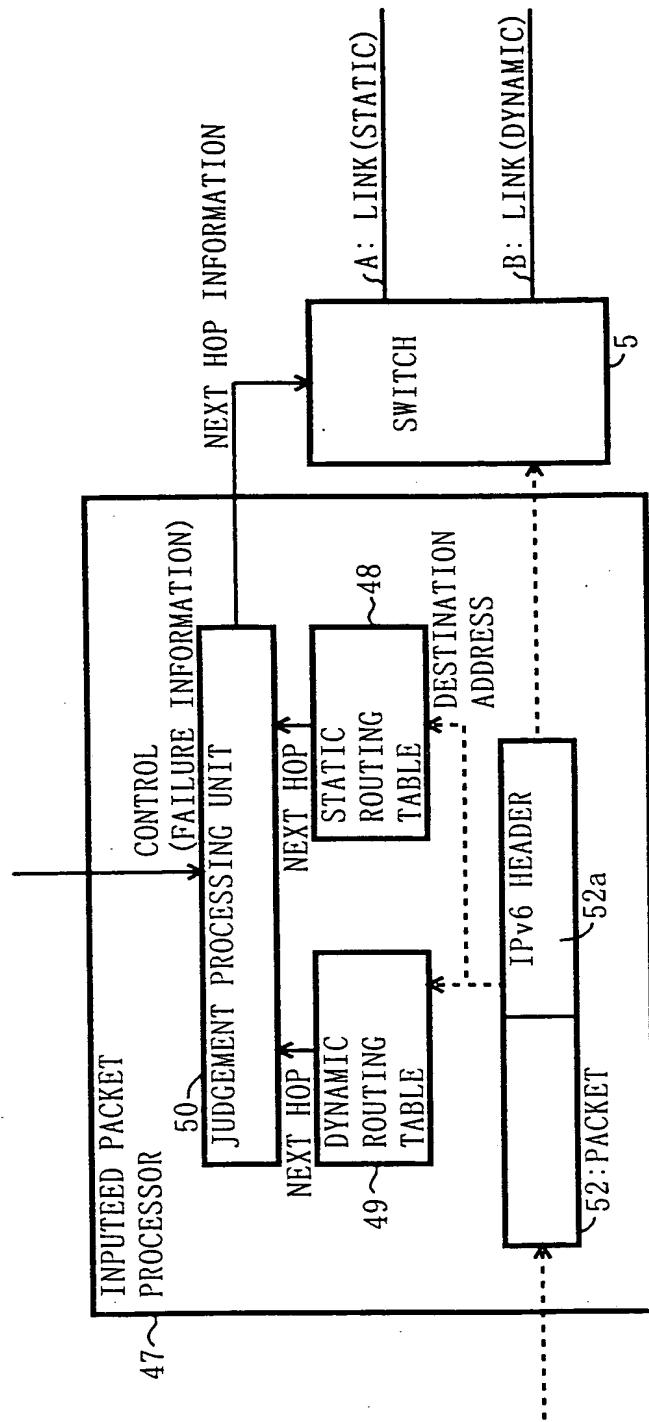


FIG. 9



F I G . 1 . O

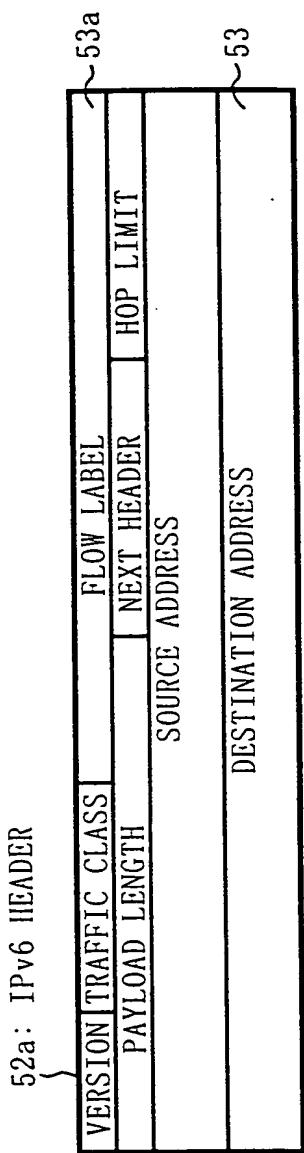


FIG. 1.1

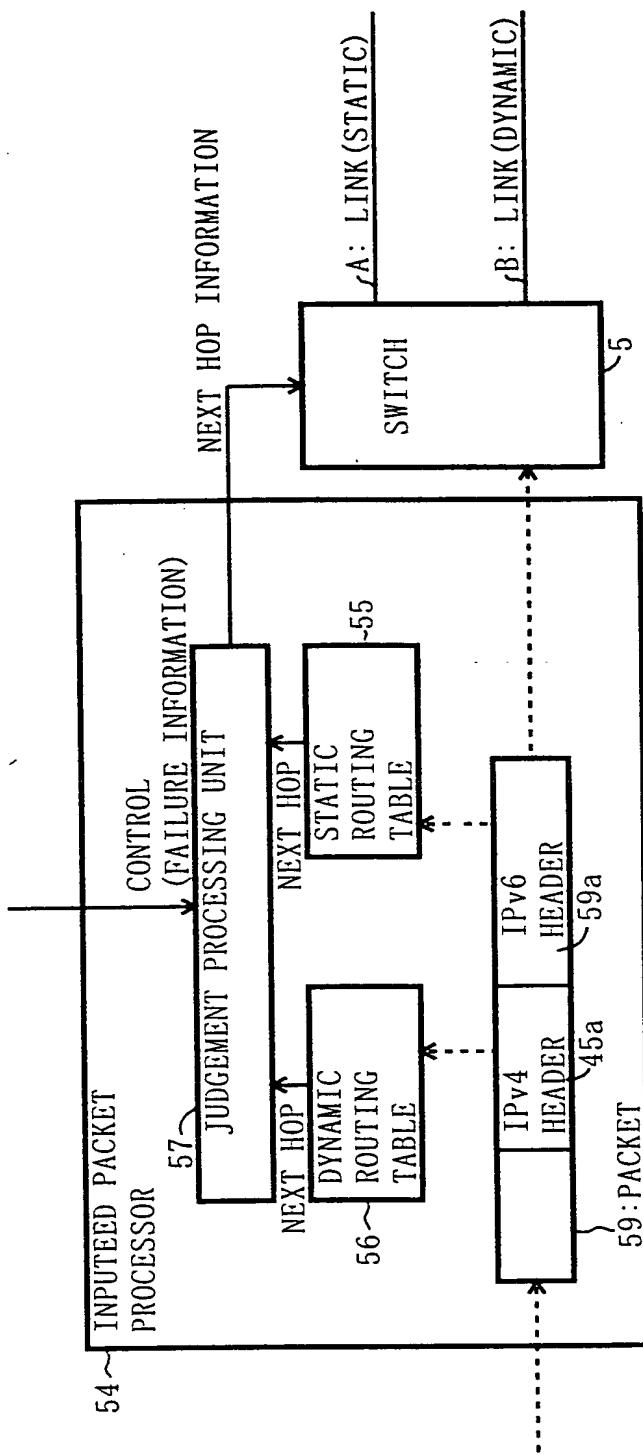
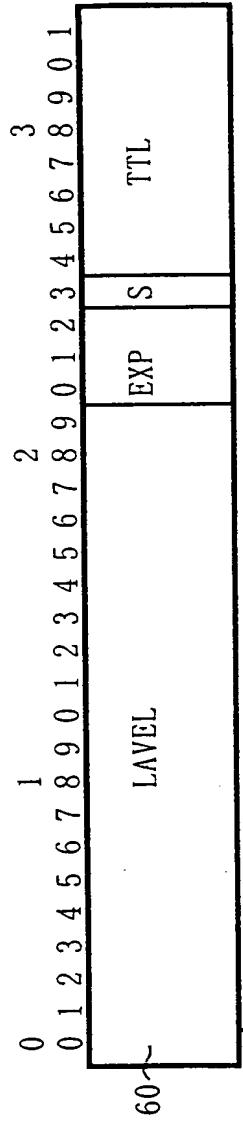


FIG. 1 2



59a: MPLS SHIM HEADER

LEVEL:	LEVEL VALUE,	20 BITS
EXP:	EXPERIMENTAL USE,	3 BITS
S:	BOTTOM OF STACK,	1 BIT
TTL:	TIME TO LIVE,	8 BITS

FIG. 1 3

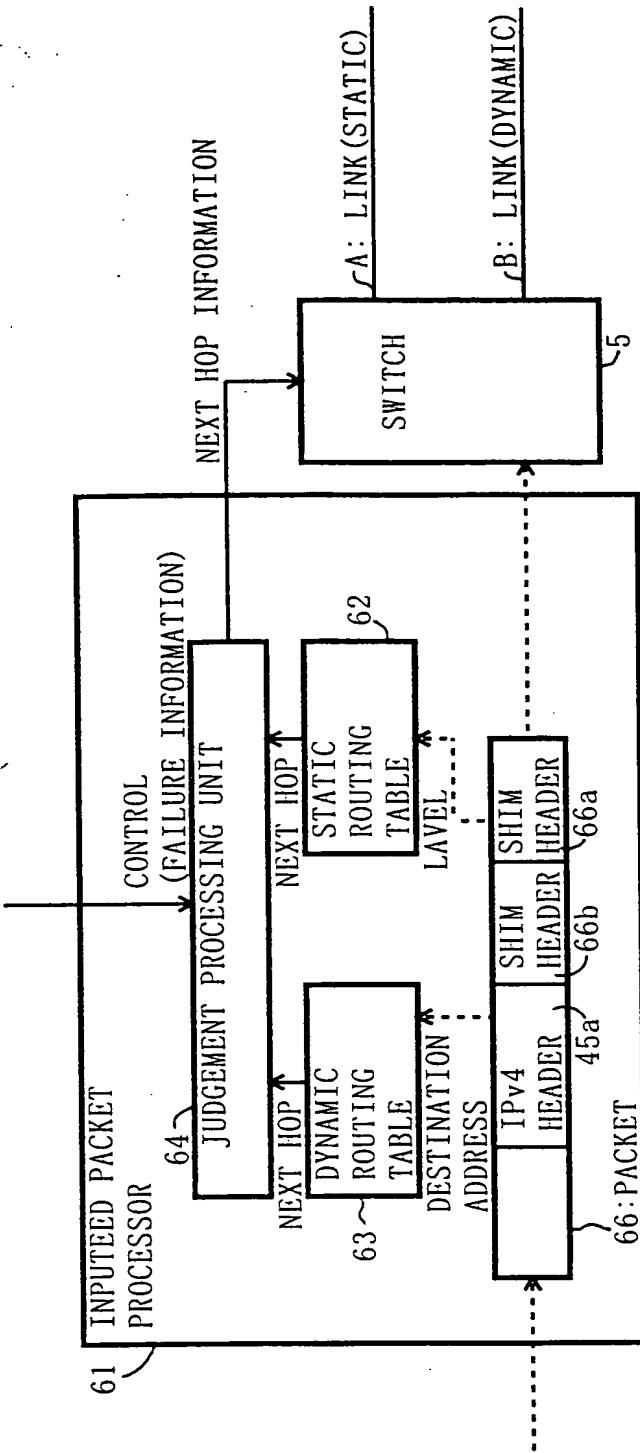


FIG. 1 A

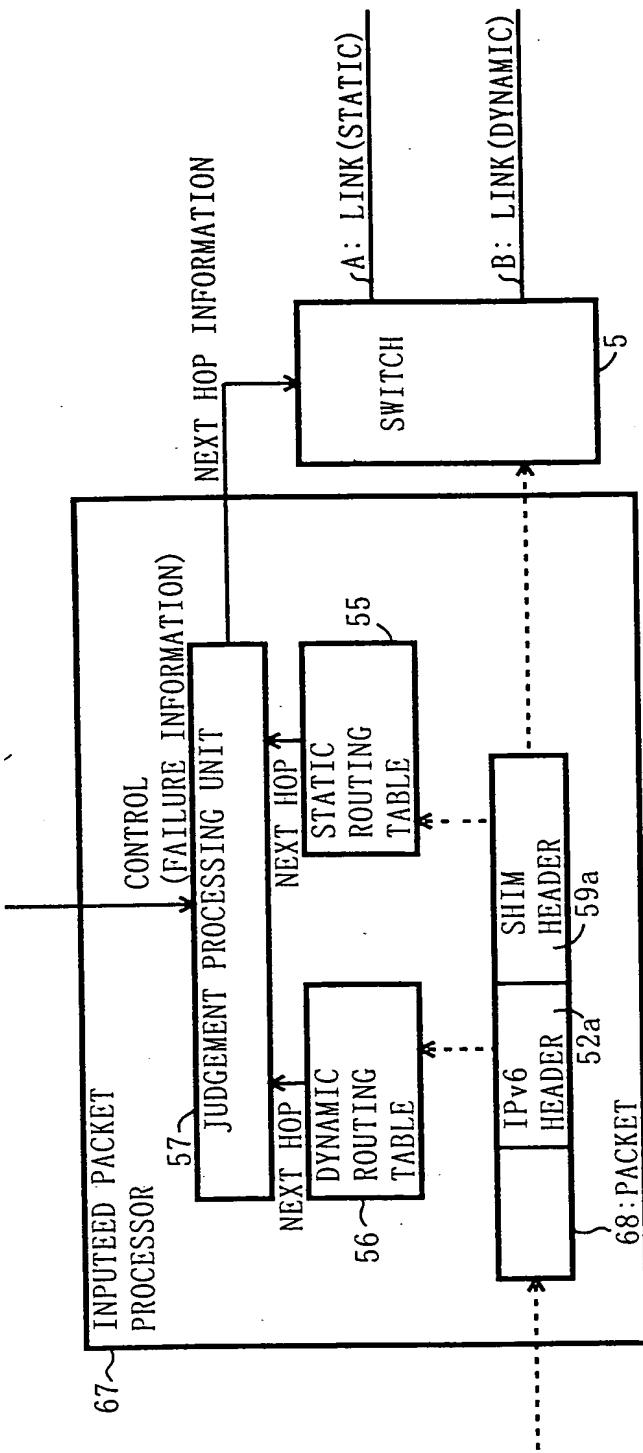
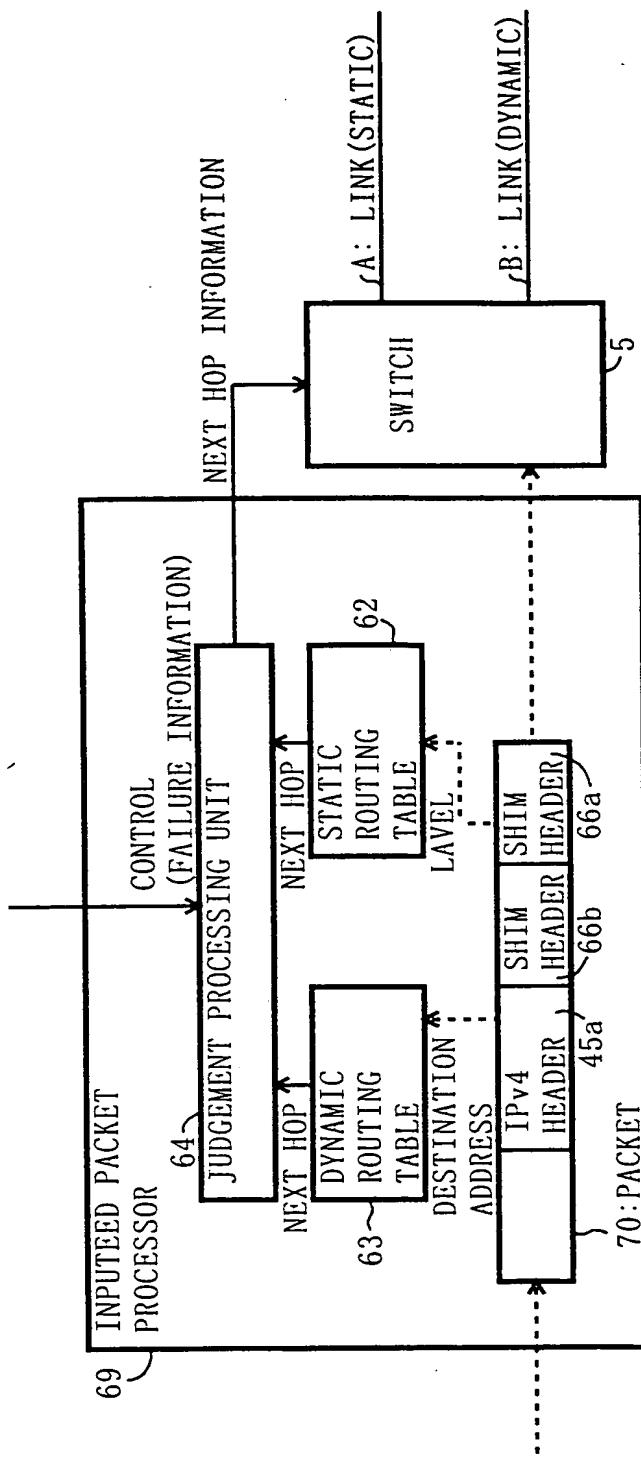
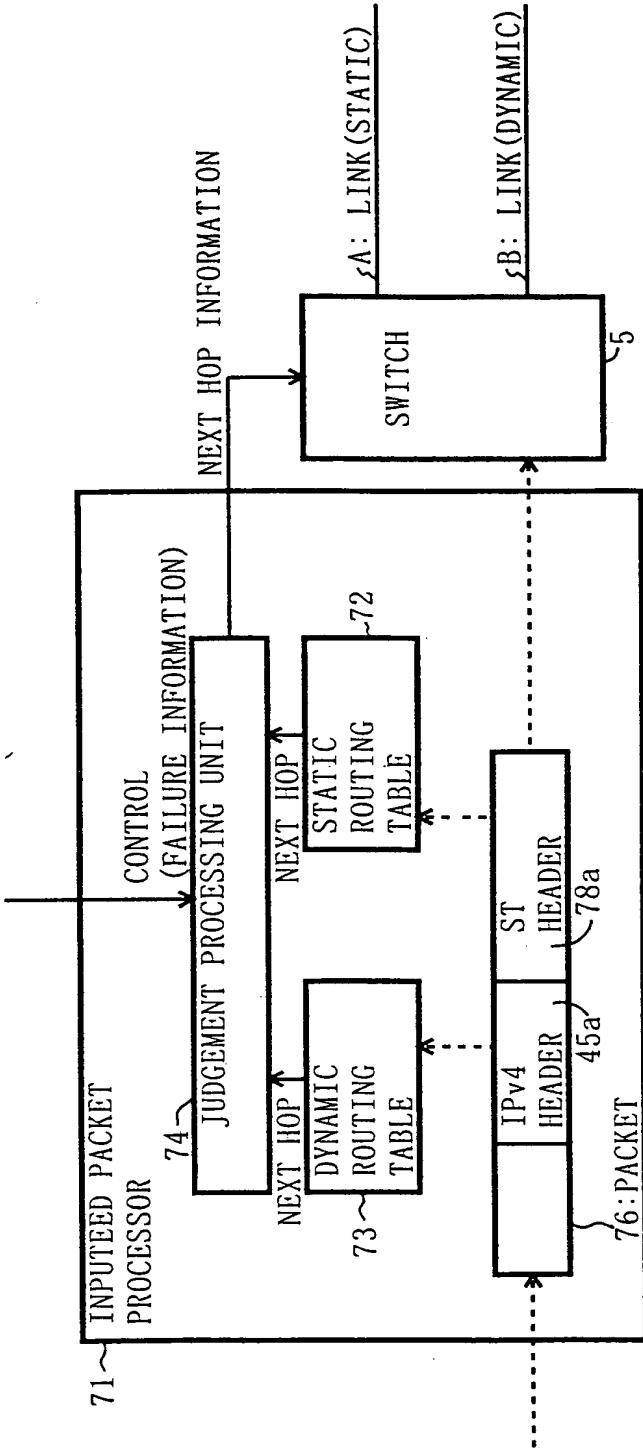


FIG. 15



F I G . 1 6



F I G . 1 7

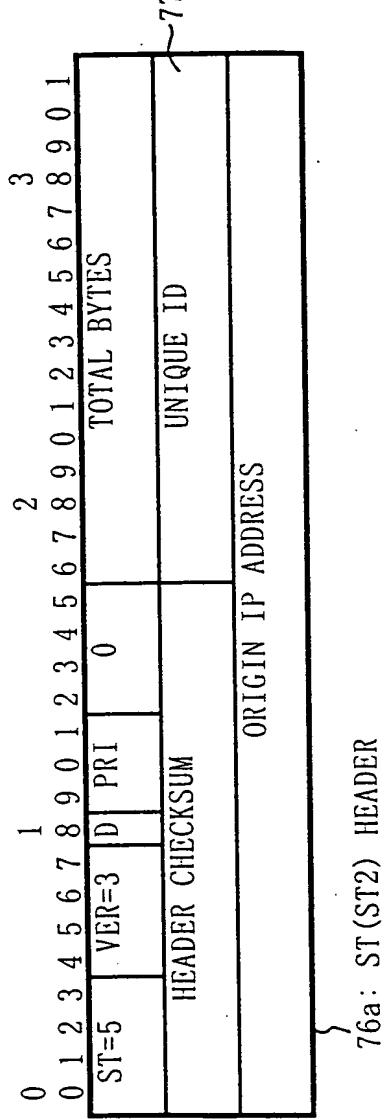


FIG. 18

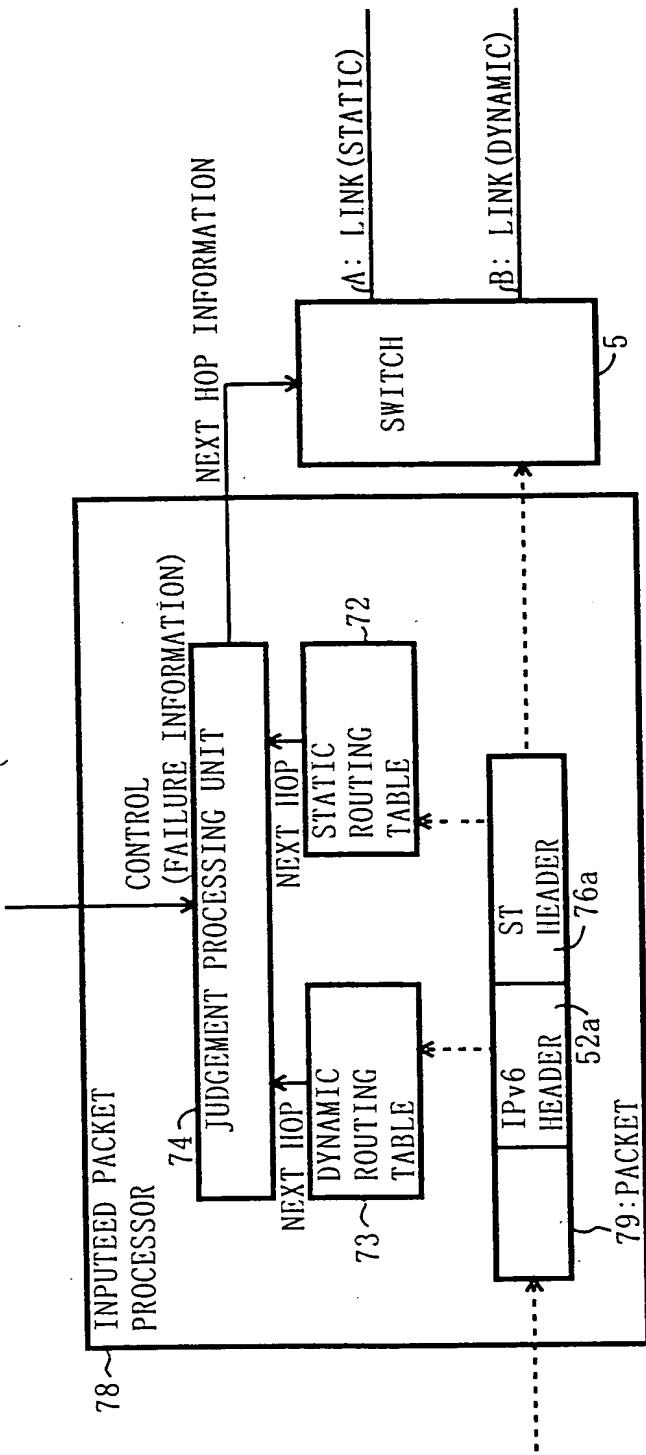


FIG. 19

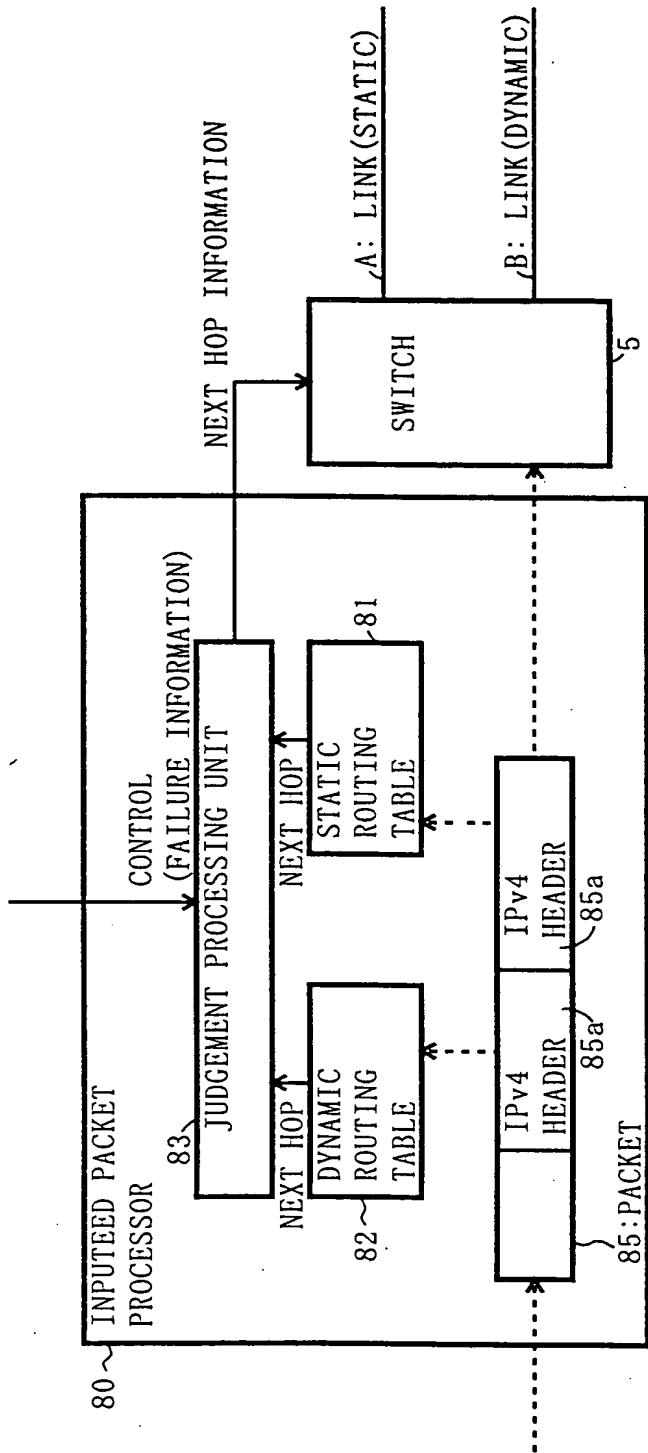


FIG. 20

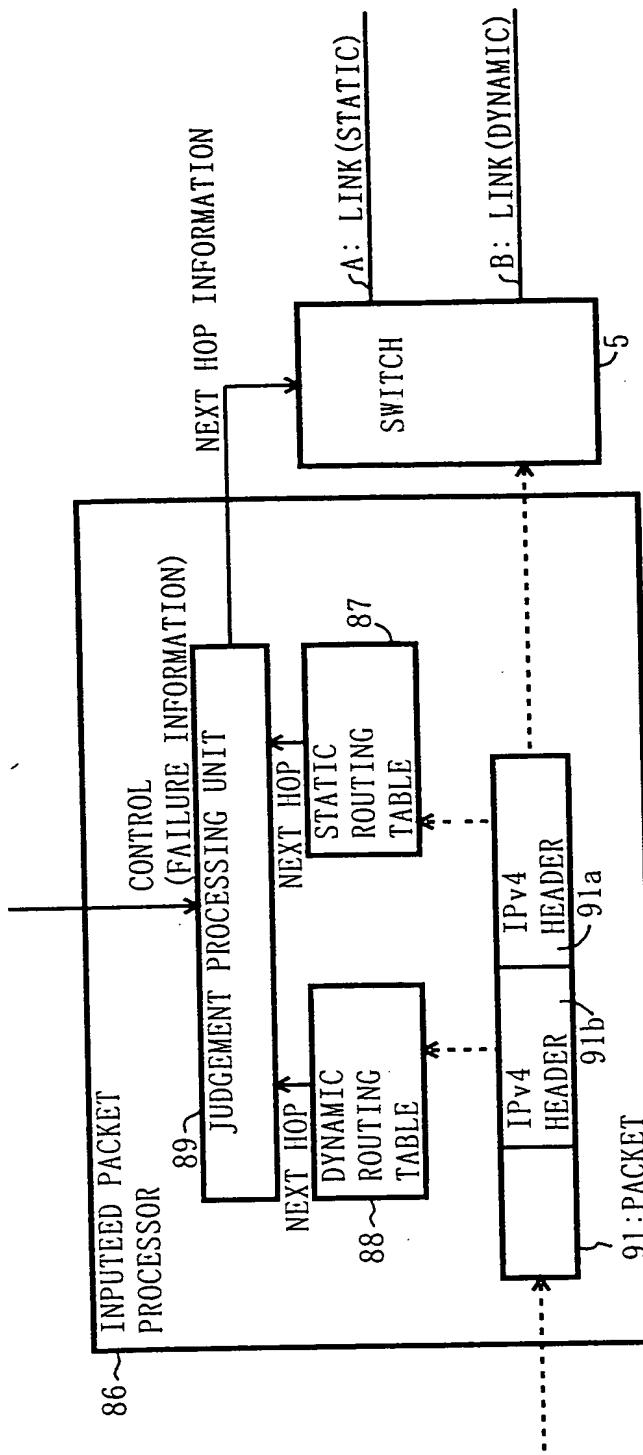


FIG. 21

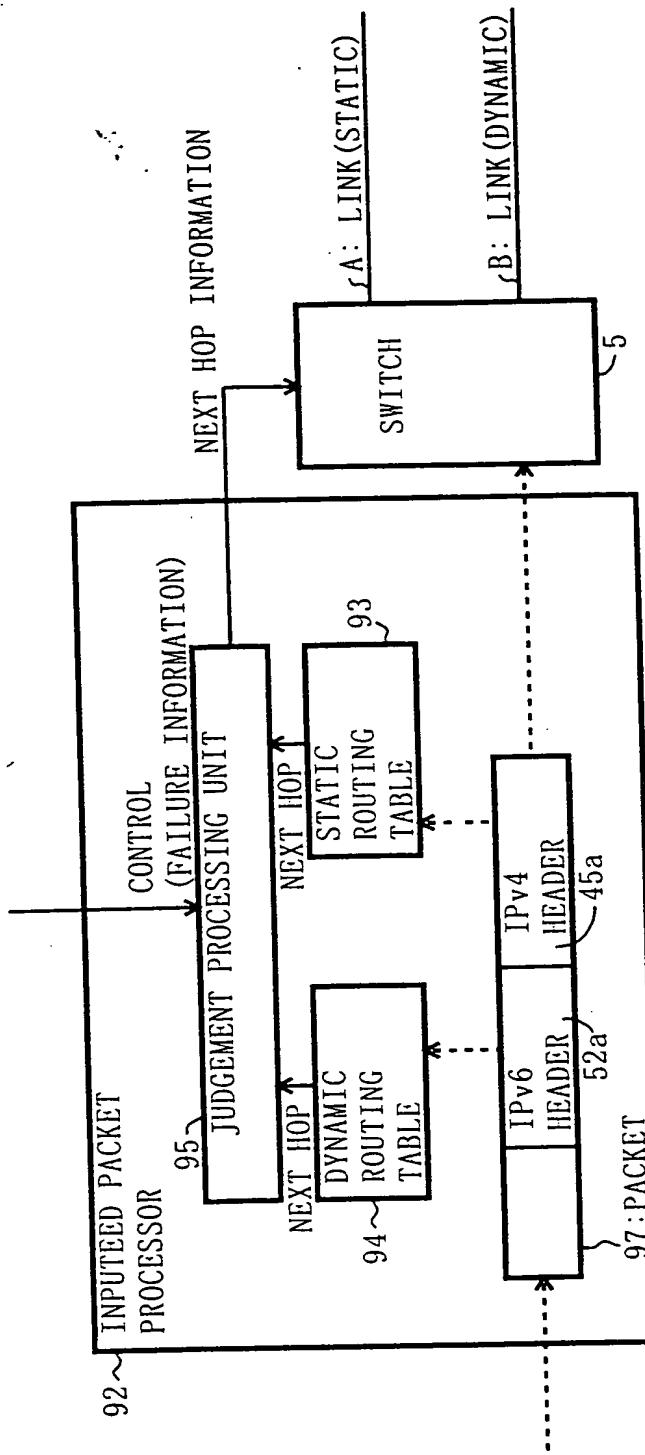


FIG. 22

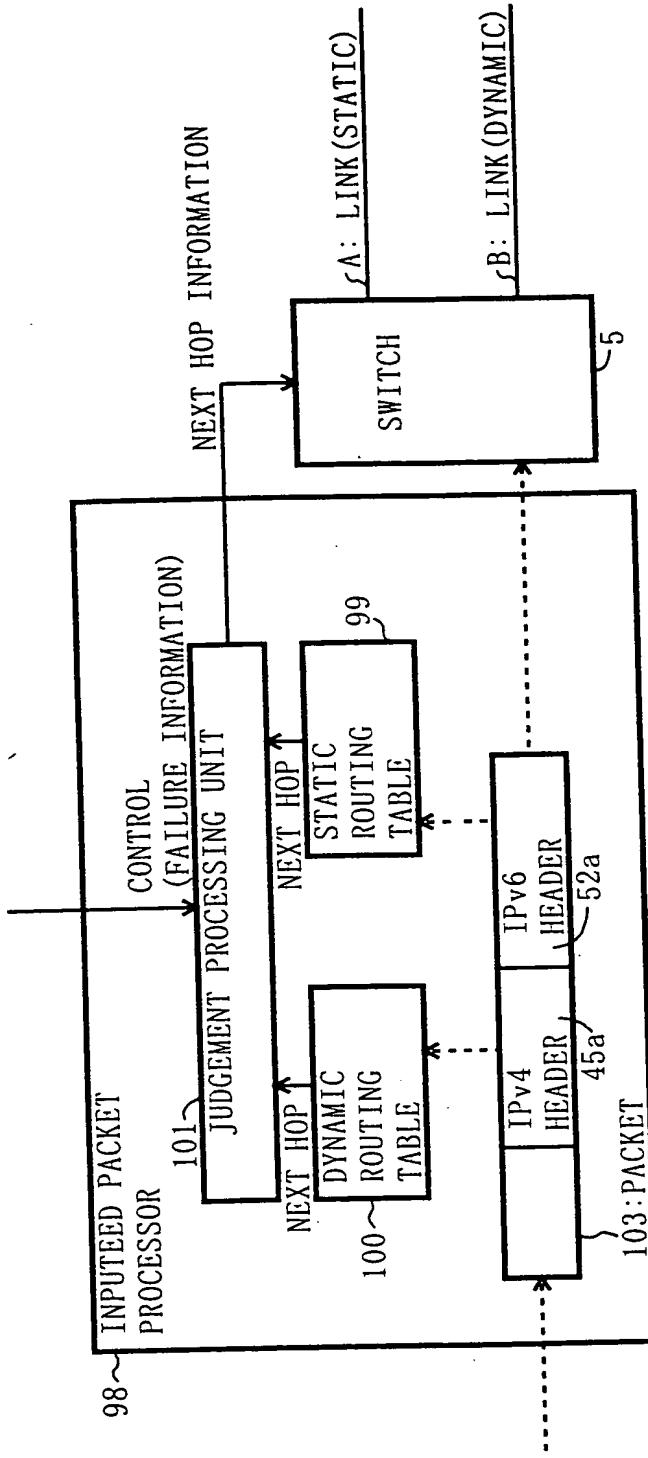


FIG. 2.3

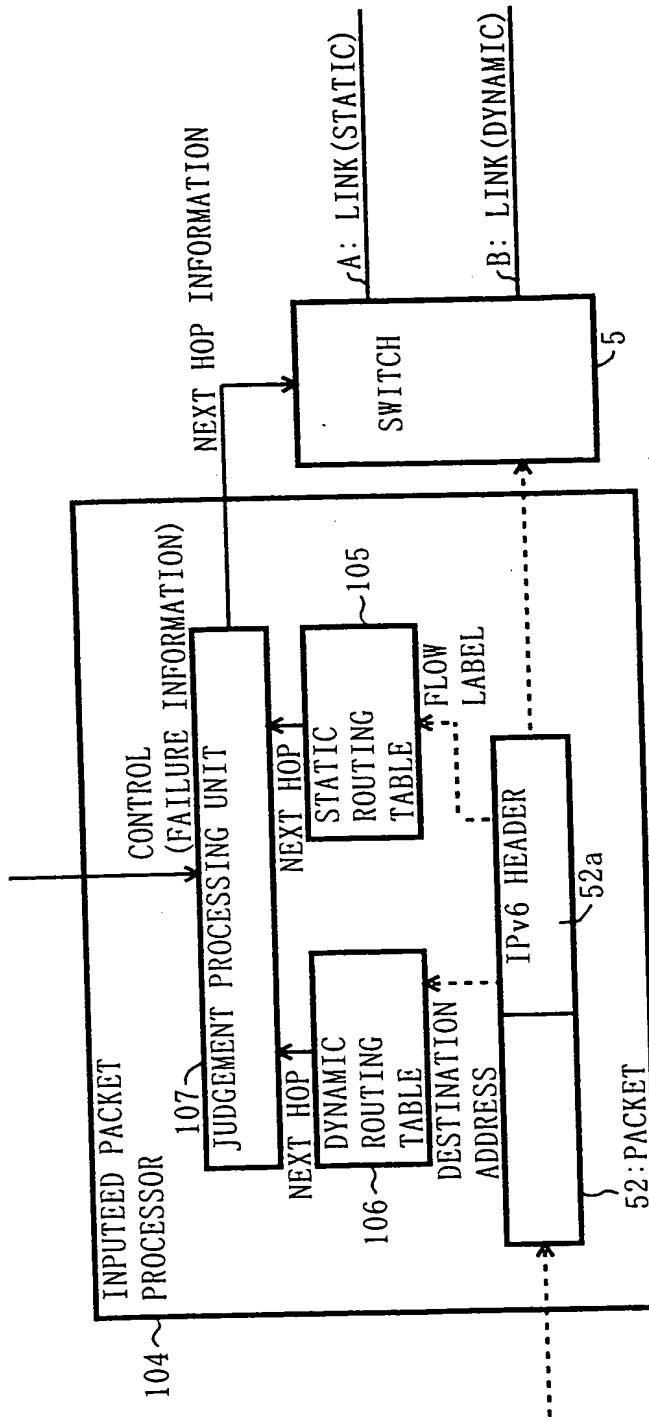


FIG. 2.4.

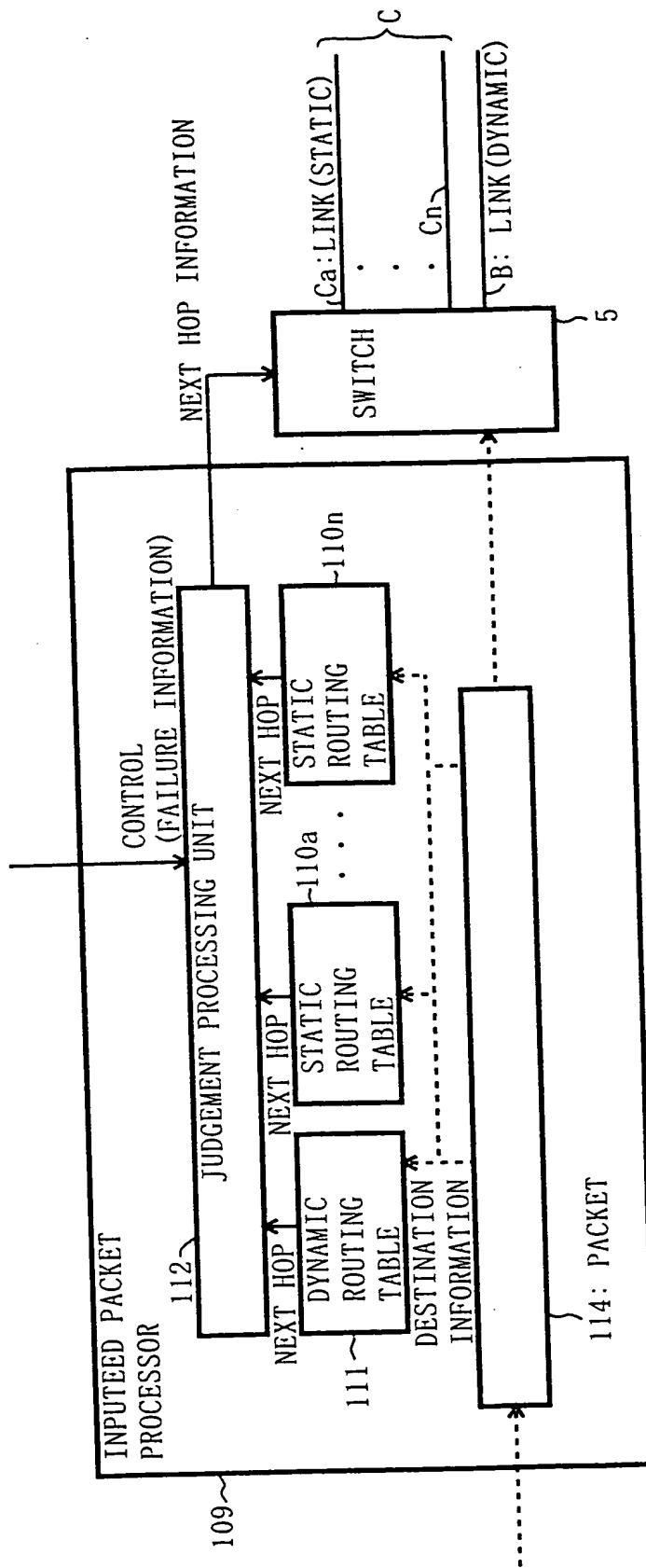


FIG. 25

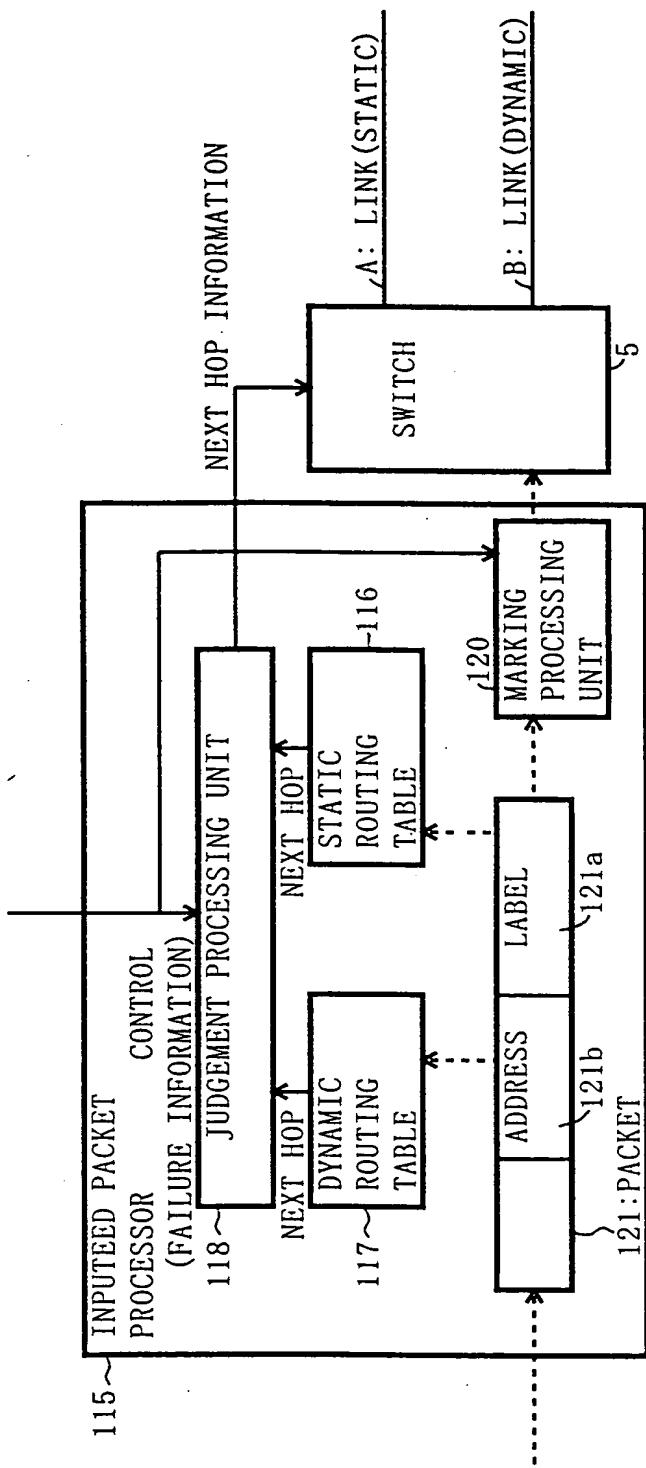
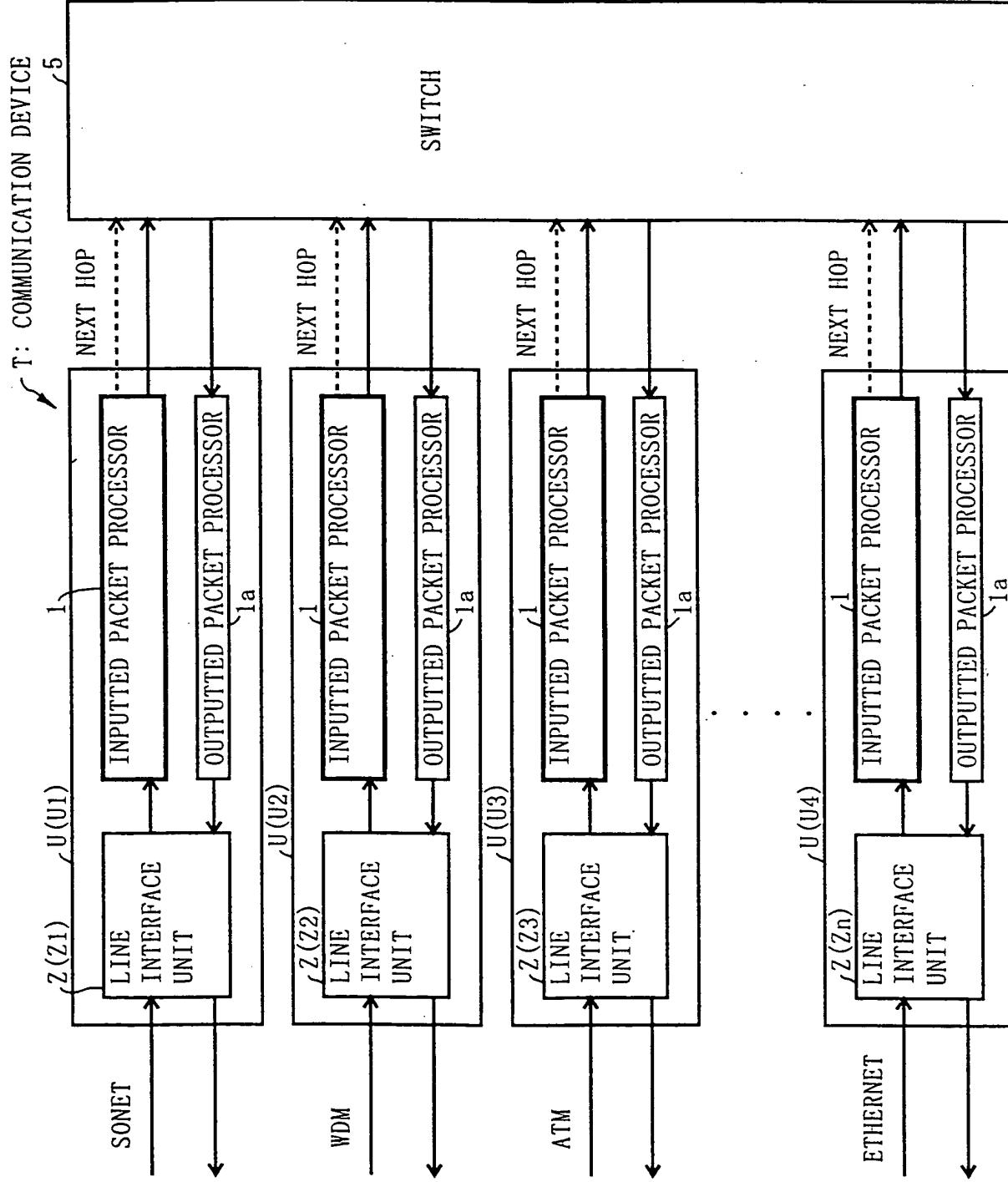


FIG. 26



EIG. 27

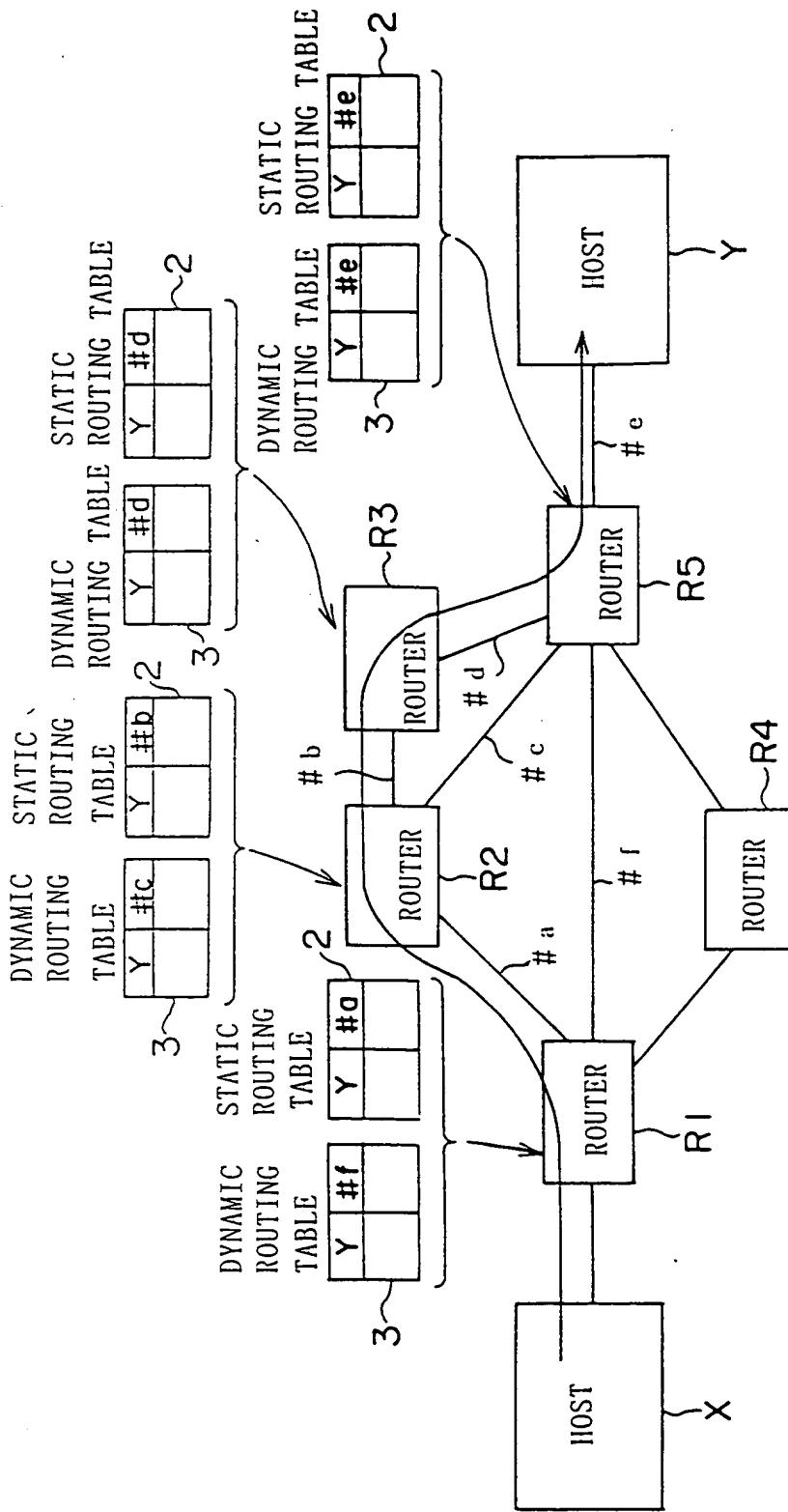


FIG. 28

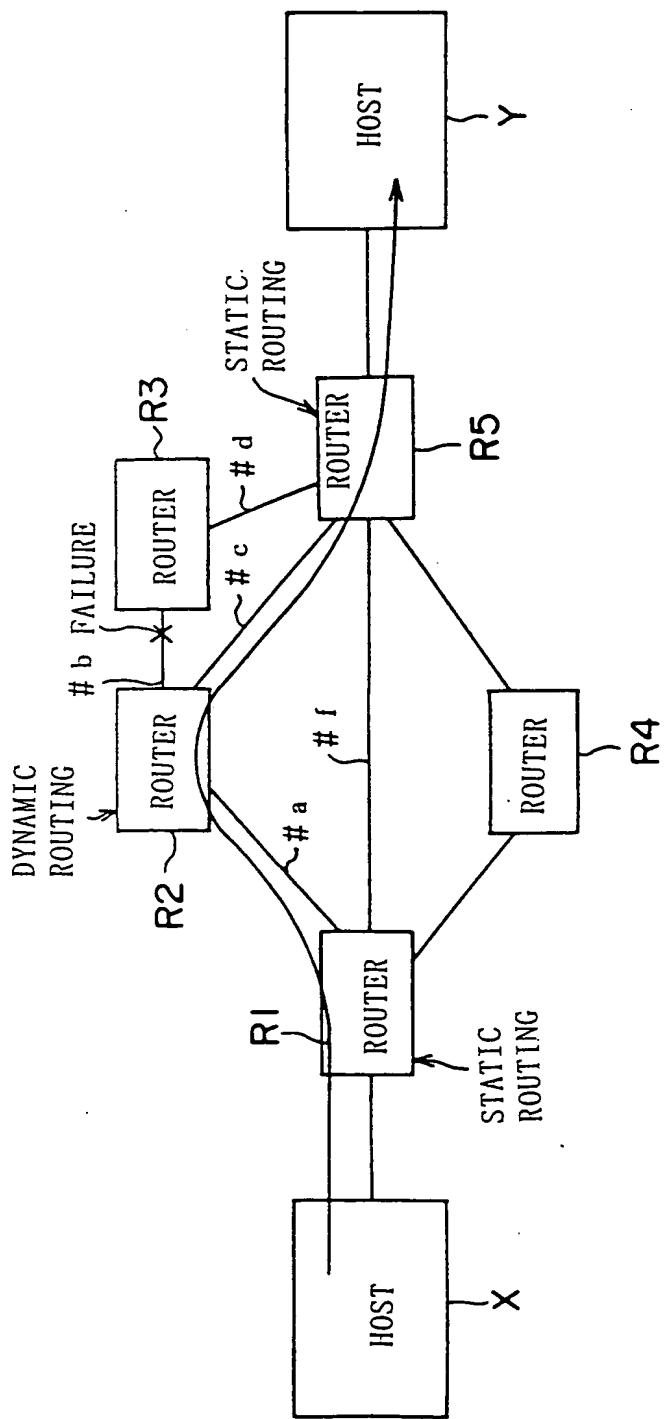


FIG. 29

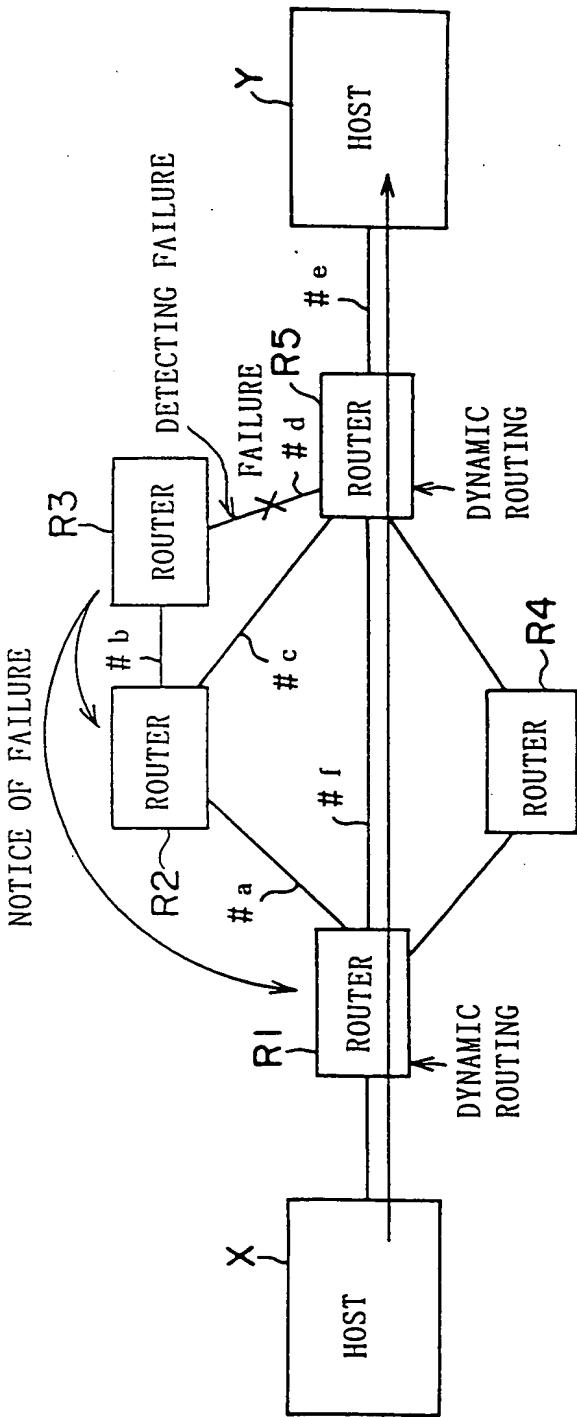


FIG. 30

